

# An EPTAS for Scheduling Fork-Join Graphs with Communication Delay

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## Problem domain

Scheduling task graphs (DAGs) with communication delays on homogeneous processors –  $P|prec, c_{ij}|C_{max}$

- No known constant approximation algorithm
- No PTAS

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## Here today

EPTAS (Efficient Polynomial Time Approximation Scheme) for **fork-join** graphs –  $P|fork - join, c_{ij}|C_{max}$

- Polynomial 2-approximation algorithm [Aussois 2018]
- Very important graph structure, realistic for many computations
- Very hard to schedule optimally (maximum degree of freedom, but order and allocation matters)

- 1 Model & approach
- 2 Simplifications
- 3 Configuration ILP
- 4 Results

1 Model & approach

2 Simplifications

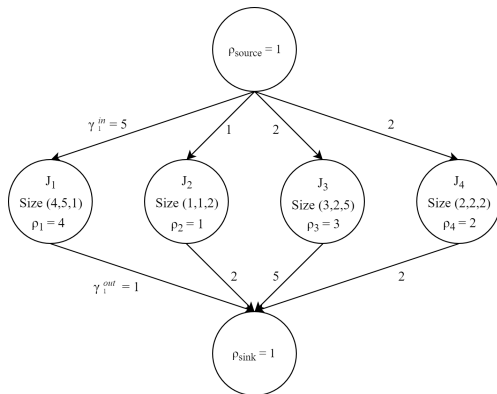
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# Scheduling problem

## Fork-join graph

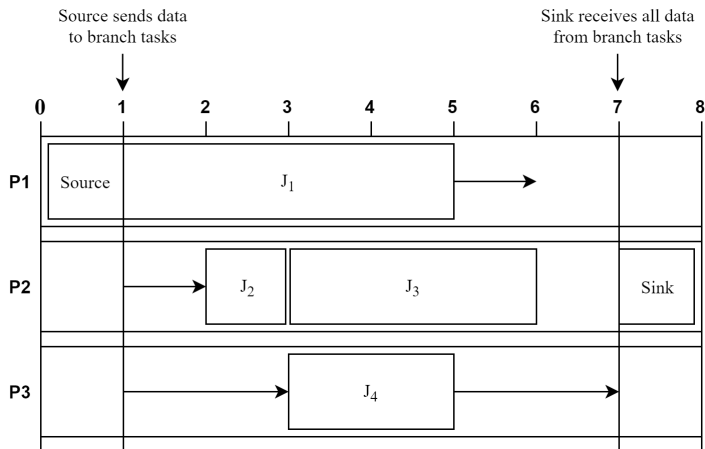
- $j_{source}, j_{sink}$ : source task, sink task
- $j \in J$ : branch task
- $(\rho_j, \gamma_j^{in}, \gamma_j^{out}) \in P \times \Gamma \times \Gamma$ 
  - task size
  - (comp., in-comm., out-comm.)
  - only remote comm. costs!
  - all  $\in \mathbb{N}_0$



## Scheduling problem

- $M$  identical processors (machines)
- minimizing makespan –  $OPT$

# Schedule



## Idea

- Formulating problem as ILP for given  $T$ , makespan  $< T$
- Binary search over makespan  $T$
- **Problem:** Solving ILP is NP-complete, exponential runtime in input size

## EPTAS Approach

- Accuracy parameter  $\epsilon > 0$
- EPTAS gives solution  $(1 + \epsilon)OPT$ 
  - Efficient complexity  $\mathcal{O}(f(1/\epsilon) \times \text{poly}(n))$ ,  $n$  not in exponent
- Transforming/simplifying problem instance
  - eventual instance size is  $f(1/\epsilon)$
- Use configuration ILP



# Approach structure

Original Instance /

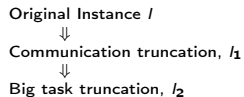
# Approach structure

Original Instance  $I$



Communication truncation,  $I_1$

# Approach structure



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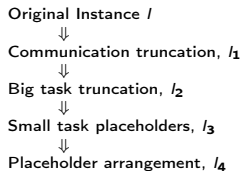


Big task truncation,  $I_2$

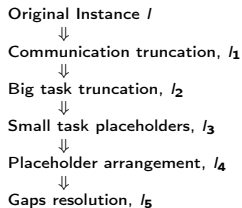


Small task placeholders,  $I_3$

# Approach structure



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Placeholder arrangement,  $I_4$



Gaps resolution,  $I_5$

⇒

Binary search over  $T$

↔ Solve configuration ILP

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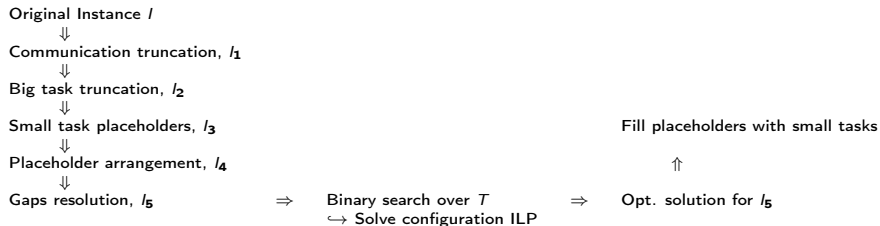
Binary search over  $T$   
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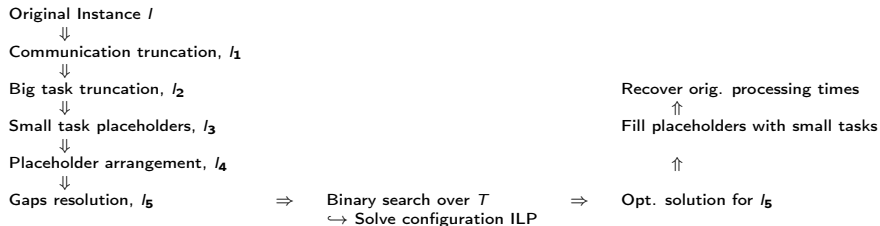
Opt. solution for  $I_5$



# Approach structure



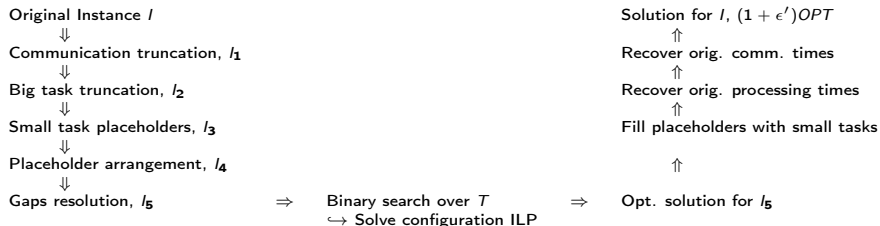
# Approach structure



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- 1 Model & approach
- 2 Simplifications**
- 3 Configuration ILP
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# Simplification 1: Communication truncation

Normalise communication times:  $\forall \gamma \in \Gamma$ : truncate value to nearest multiple of  $\epsilon T \Rightarrow$  new instance  $I_1$

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## Lemma (Communication truncation)

Let  $I_1$  be the instance obtained after this communication times truncation step, and  $T_1$  be the minimum makespan for  $I_1$ . Then

$$T_1 \leq OPT \leq T_1 + 2\epsilon T$$

# Simplification 2: Big tasks

## Distinguish tasks

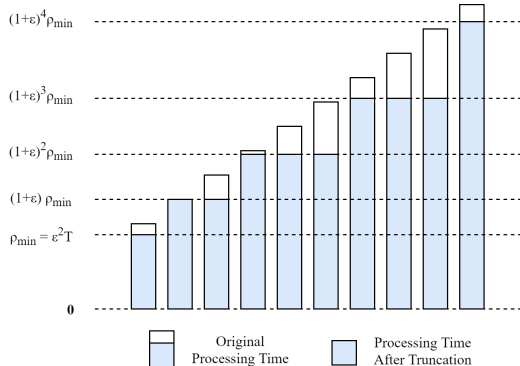
- Big tasks:  $\rho_i \geq \epsilon^2 T$
- Small tasks:  $\rho_i < \epsilon^2 T$

## Big tasks

Normalise/truncate computation times to

$$\{(1 + \epsilon)^n \epsilon^2 T \mid n \in \mathbb{N}_0\}$$

$\Rightarrow$  new instance  $I_2$





# Simplification 2: Big tasks

## Distinguish tasks

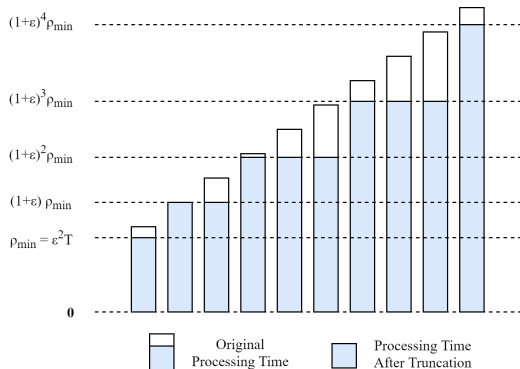
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## Big tasks

Normalise/truncate computation times to

$$\{(1 + \epsilon)^n \epsilon^2 T \mid n \in \mathbb{N}_0\}$$

$\Rightarrow$  new instance  $I_2$



## Lemma (Big task truncation)

Let  $I_2$  be the instance obtained by applying this task time truncation step to  $I_1$ , and  $T_2$  be the minimum schedule makespan for  $I_2$ .

$$T_2 \leq T_1 \leq T_2 + \epsilon T$$

## Simplification 3: Small task placeholders

- Remove all small tasks  $J_{small}^{\gamma^{in}, \gamma^{out}}$
- Replace by placeholders tasks with uniform  $\rho = \epsilon^3 T$
- Number of placeholders

$$\left\lceil \frac{\sum_{j \in J_{small}^{\gamma^{in}, \gamma^{out}}} \rho_j}{\epsilon^3 T} \right\rceil$$

⇒ new instance  $I_3$

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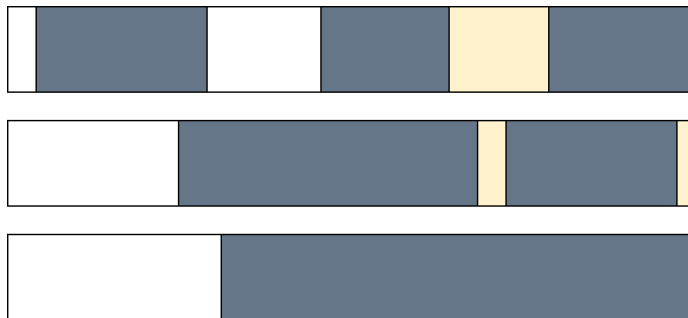
### Lemma (Small tasks placeholders)




Let  $I_3$  be the instance obtained after applying this small task replacement step to  $I_2$ , and  $T_3$  be the minimum schedule makespan for  $I_3$ .

$$T_3 - \epsilon T \leq T_2 \leq T_3 + 2\epsilon T$$

# Simplification 3: Small task placeholders – reverse step

## Schedule with placeholders

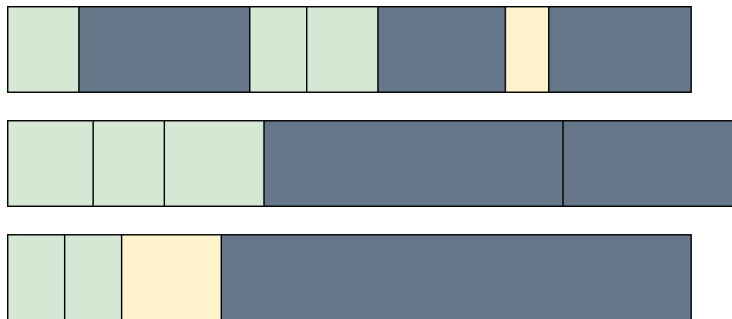





-  Space occupied by placeholders for tasks with particular  $\gamma^{\text{in}}, \gamma^{\text{out}}$
-  Space occupied by placeholders for tasks with some smaller  $\gamma^{\text{in}}, \gamma^{\text{out}} - \delta$
-  All other Tasks

# Simplification 3: Small task placeholders – reverse step

Schedule with small tasks recovered

Using a NextFit algorithm per communication size  $(\gamma^{in}, \gamma^{out})$



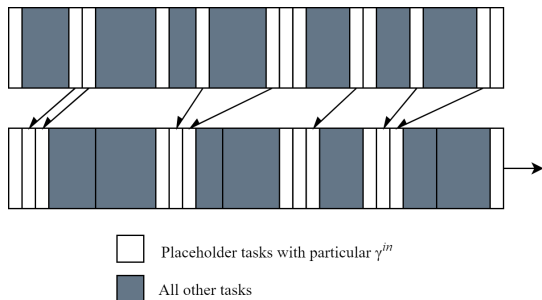
-  Tasks with  $\gamma^{in}, \gamma^{out}$  packed into spaces occupied by their placeholders
-  Space for tasks with  $\gamma^{in}, \gamma^{out} - \delta$
-  All other Tasks

## Simplification 4: Placeholder Arrangement

Placeholders created in previous step forced into groups

- placeholders  $\forall \gamma_i, \int_{small}^{\gamma_i}$ , forced into groups of  $\frac{1}{\epsilon}$
- their total processing time  $\epsilon^2 T$
- remainder in *stub* group of  $\leq \frac{1}{\epsilon}$  placeholders

$\Rightarrow$  new instance  $I_4$

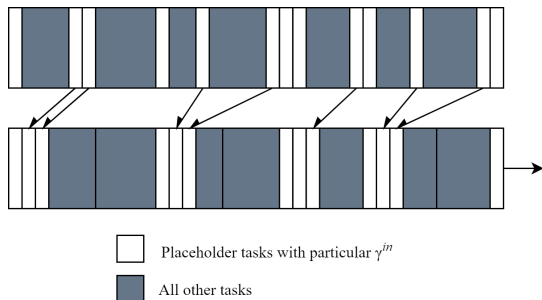


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- remainder in *stub* group of  $\leq \frac{1}{\epsilon}$  placeholders

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### Lemma (Placeholder grouping)

Let  $I_4$  be the instance of the problem with this restriction to the solution, and  $T_4$  be the minimum schedule makespan for  $I_4$ .

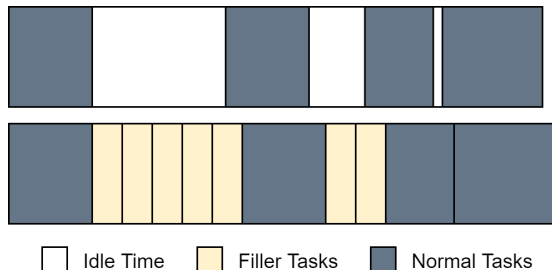
$$T_4 - \epsilon T \leq T_3 \leq T_4$$

## Simplification 5: Gap sizes

Idle time gaps scaled to multiples of  $\epsilon^2 T$

- same size as small task placeholders
- use filler tasks of size to  $\epsilon^2 T$  to fill idle gaps

⇒ new instance  $I_5$



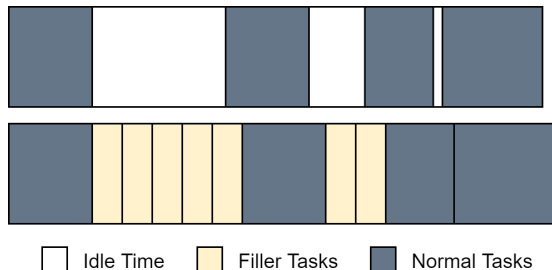


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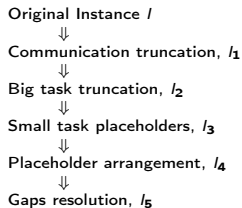
### Lemma (Idle gap scaling)

Let  $I_5$  be the instance where task start times are restricted in this way, and let  $T_5$  be the minimum schedule makespan for  $I_5$ .

$$T_5 - \epsilon^2 T \leq T_4 \leq T_5$$

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Binary search over  $T$

↔ Solve configuration ILP

Instance  $I_5$  is described by (for given  $T$ ):

$N_{\rho, \gamma^{in}, \gamma^{out}} \quad \forall (\rho, \gamma^{in}, \gamma^{out})$ : number (multiplicity) of tasks of size  $(\rho, \gamma^{in}, \gamma^{out})$

- total number of different sizes is  $\mathcal{O}(\text{poly}(1/\epsilon))$
- multiplicity is also in  $\mathcal{O}(\text{poly}(1/\epsilon))$

Finding valid schedule for  $I_5$  for given  $T$ :

Use configuration ILP

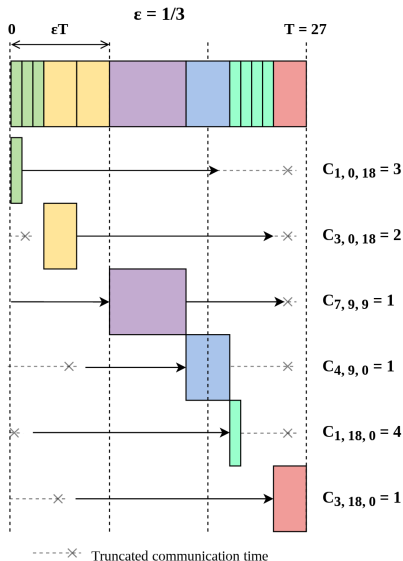
- where configuration is order of task sizes and their multiplicity

# Configurations

## Configuration $C$ :

- set of tasks (multiset of task sizes  $(\rho, \gamma^{in}, \gamma^{out})$ )
- with an assumed order, with arrival time of last edge  $< T$
- to be scheduled onto a single processor

$C$ : set of all needed (possible) configurations



# Constraints

Decision variables for ILP:

$x_C \forall C \in \mathbf{C}$  select the number of each configuration used

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All tasks scheduled:

$$\sum_{c \in \mathbf{C}} x_C C_{\rho, \gamma^{in}, \gamma^{out}} \geq N_{\rho, \gamma^{in}, \gamma^{out}} \quad \forall (\rho, \gamma^{in}, \gamma^{out})$$

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$$\sum_{C \in \mathbf{C}} x_C C_{\rho, \gamma^{in}, \gamma^{out}} + S_{\rho, \gamma^{in} + \Delta, \gamma^{out}}^{>in} + S_{\rho, \gamma^{in}, \gamma^{out} + \Delta}^{>out} \\ - S_{\rho, \gamma^{in}, \gamma^{out}}^{>in} - S_{\rho, \gamma^{in}, \gamma^{out}}^{>out} \geq N_{\rho, \gamma} \\ \forall (\rho, \gamma^{in}, \gamma^{out})$$

# Obtaining a configuration

Straight forward:

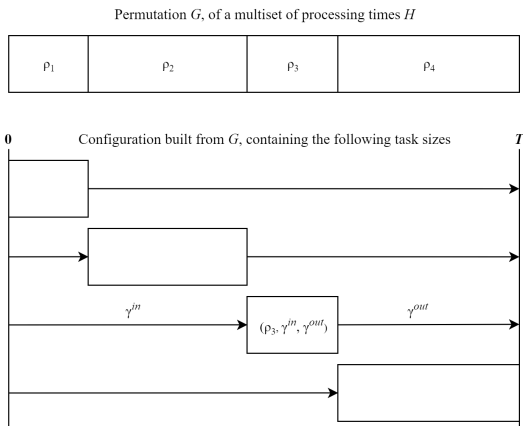
One  $C$  for every valid permutation of task sizes/multiplicities

Better:

Only considering permutations of **execution cost**  $\rho$  & multiplicities

Possible because:

shorter communications can be put in slot for larger communications



# Obtaining a configuration

Set of configurations  $\mathbf{C}$ :

- only valid  $C$ , i.e. last edge out  $< T$
- only maximal  $C$ , i.e. no  $C \in \mathbf{C}$  is subset of other  $C' \in \mathbf{C}$
- only dominating comm sizes  $(\gamma^{in}, \gamma^{out})$

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Lemma ( $\mathbf{C}$  is complete)

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Lemma (Number of configurations)

*The number of configurations  $|\mathbf{C}| = 2^{\mathcal{O}(1/\epsilon^2 \log 1/\epsilon)}$*

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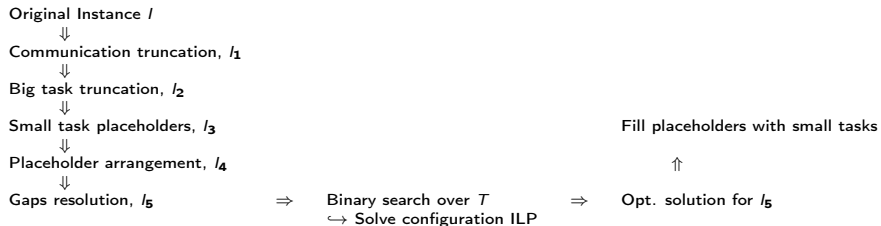


Binary search over  $T$   
↔ Solve configuration ILP

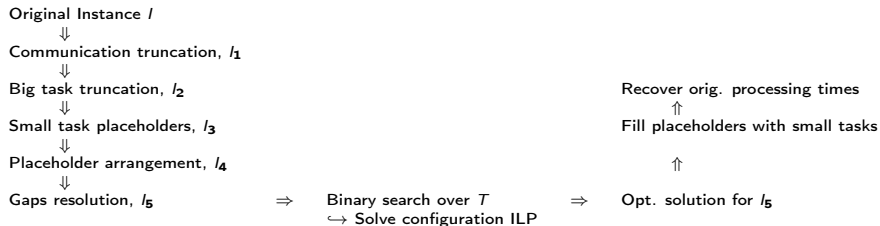


Opt. solution for  $I_5$

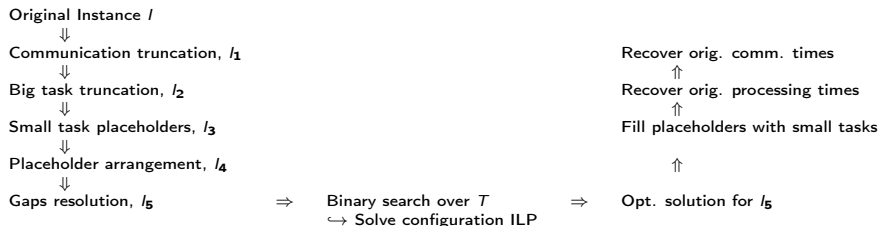
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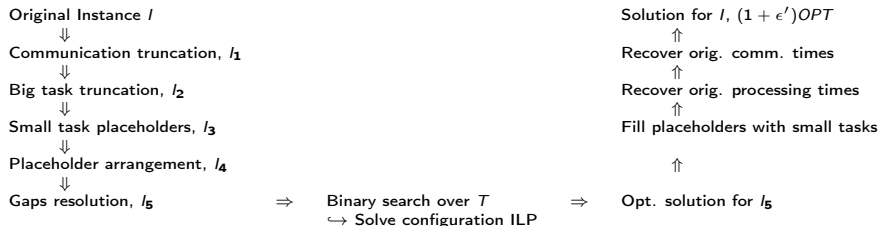
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Combining the inequalities from 5 simplification lemmas:

$$(1 - 2\epsilon - \epsilon^2)T \leq OPT \leq (1 + 5\epsilon)T$$

Final schedule obtained has makespan:

$$\frac{(1 + 5\epsilon)}{(1 - 2\epsilon - \epsilon^2)} OPT$$

To obtain accuracy parameter  $\epsilon'$  set

$$1 + \epsilon' = \frac{(1 + 5\epsilon)}{(1 - 2\epsilon - \epsilon^2)}$$

Let  $N = |J|$  be number of tasks in input of instance  $I$ .

- Simplifying instance & obtaining inputs for ILP takes  $\mathcal{O}(N)$  time
- Using binary search to find  $T$ , EPTAS running is  $(\text{ILP} + \mathcal{O}(N)) \cdot \log(N)$

With results from [1] runtime of ILP is:

$$2^{\mathcal{O}(1/\epsilon^3 \log^2 1/\epsilon)} \mathcal{O}(\log N)$$

[1] K. Jansen, L. Rohwedder. "On integer programming and convolution". arXiv:1803.04744 (2018)

## Theorem

The *EPTAS* finds a schedule with makespan no longer than:

$$\frac{(1 + 5\epsilon)}{(1 - 2\epsilon - \epsilon^2)} OPT$$

in time:

$$2^{\mathcal{O}(1/\epsilon^3 \log^2 1/\epsilon)} \mathcal{O}(\log^2 N) + \mathcal{O}(N \log N)$$

Future work:

- Versions for fork graphs and for join graphs
- Using approach for release time and deadline scheduling